

#3

/ 100

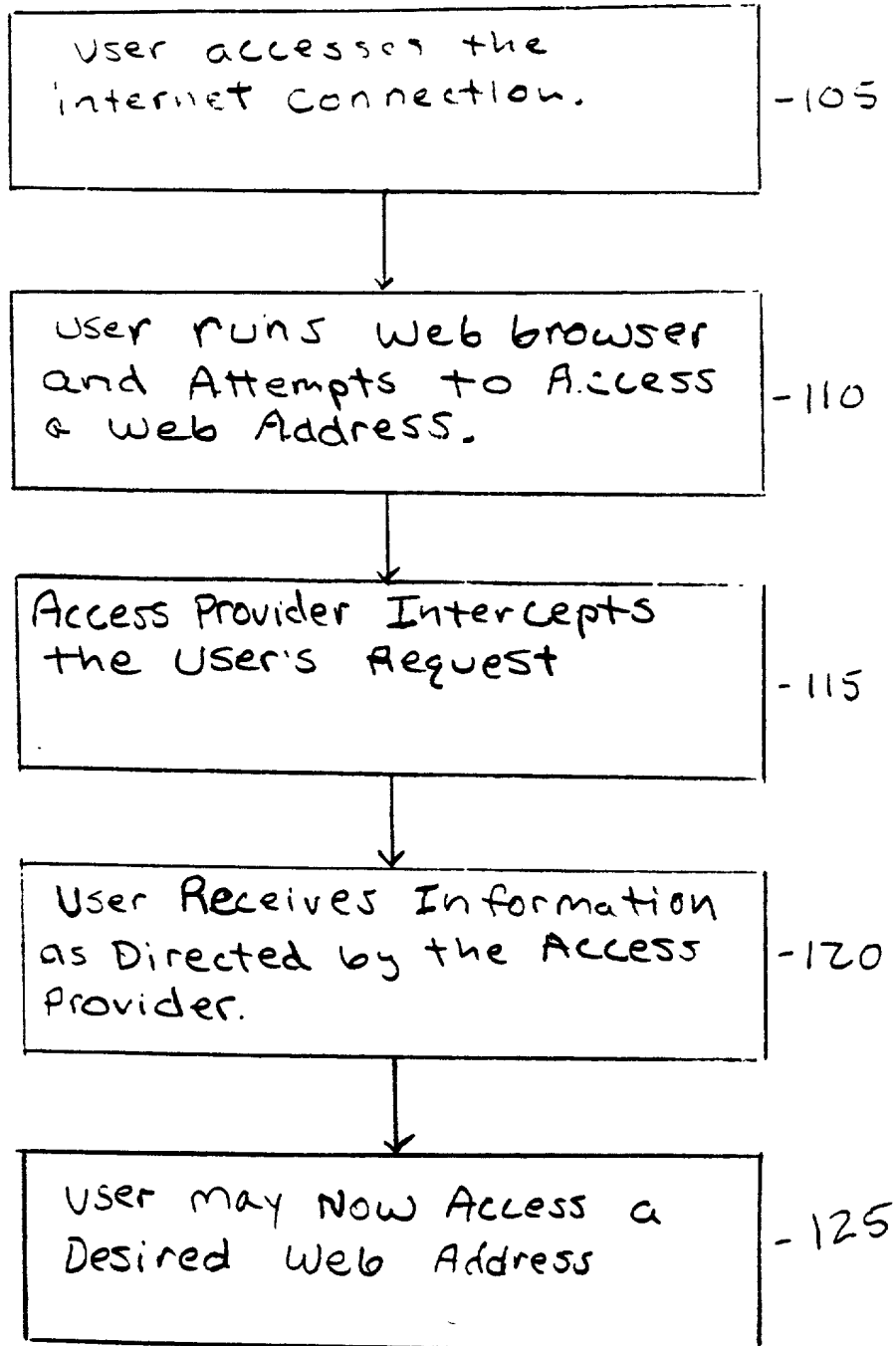


Fig 1

Prior Art

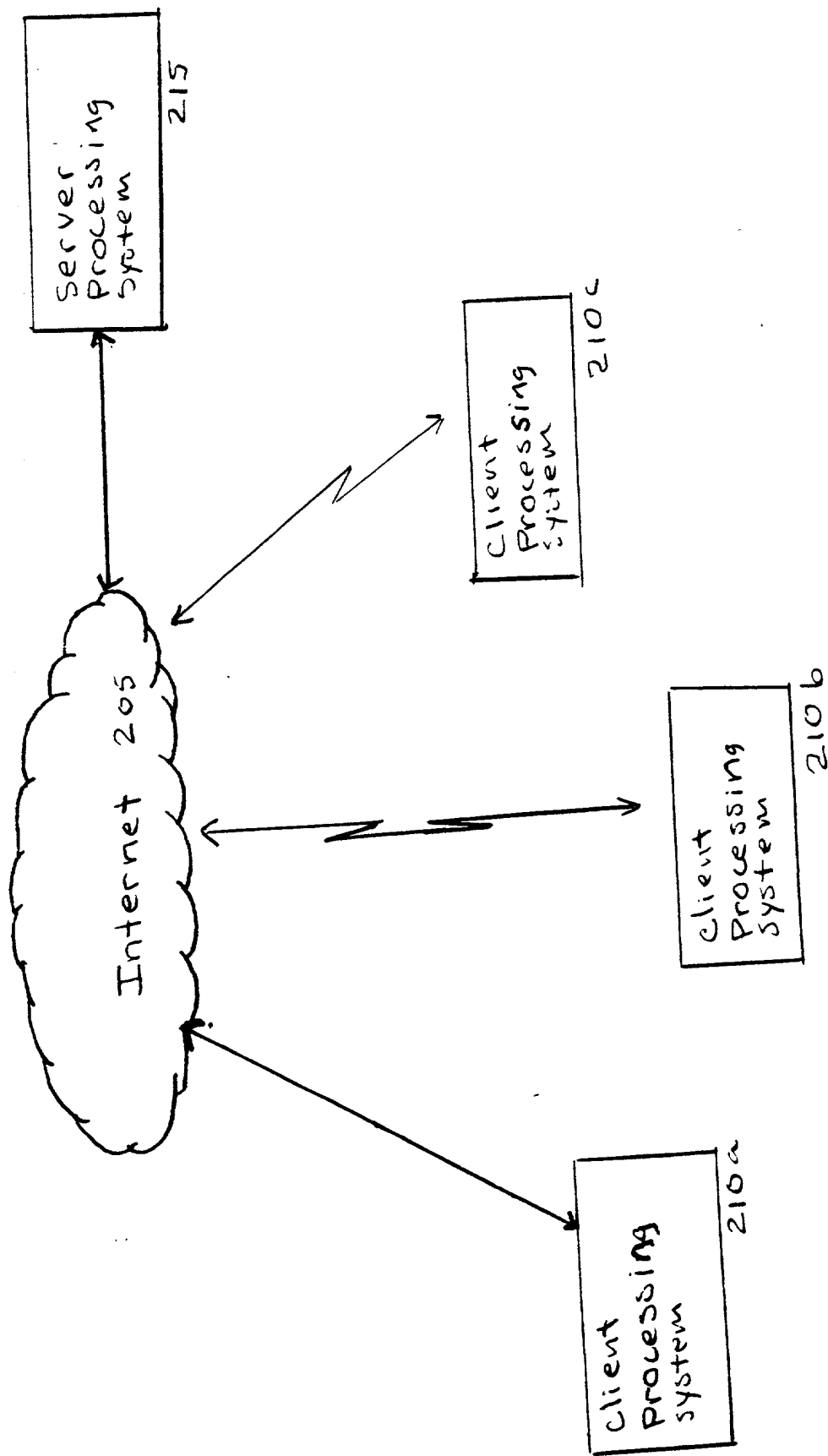
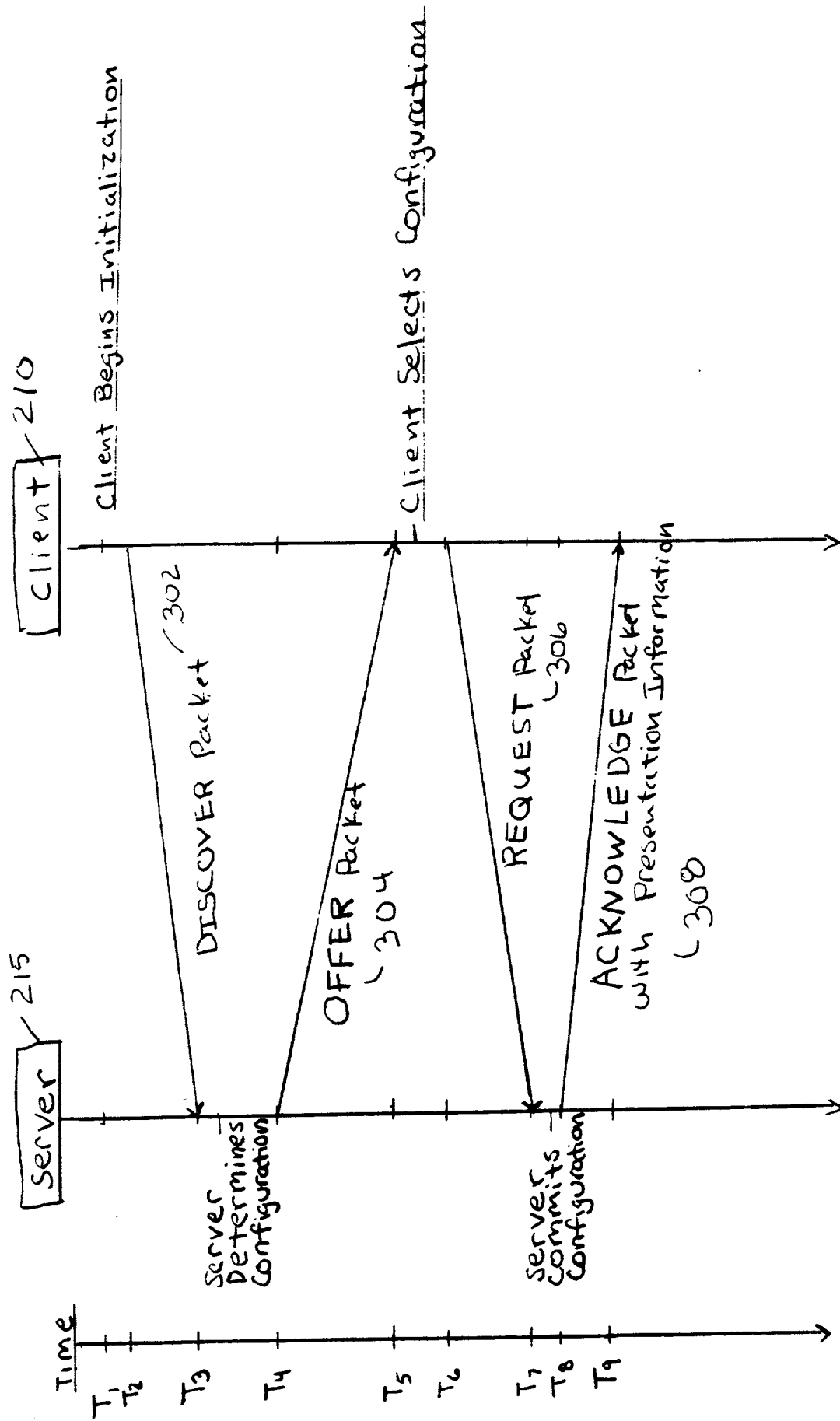


Fig. 2



Message Flow Time Line

Fig 3

400

400

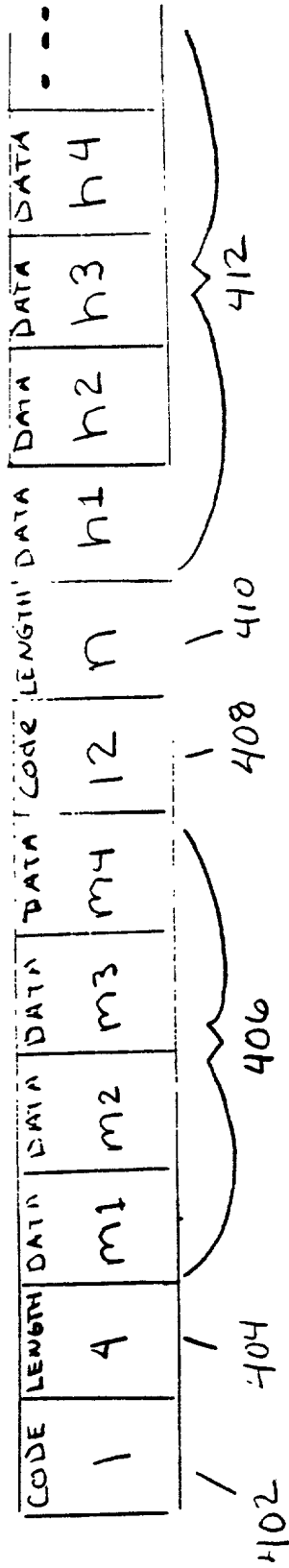


FIG. 4A

DHCP Packet Format



500

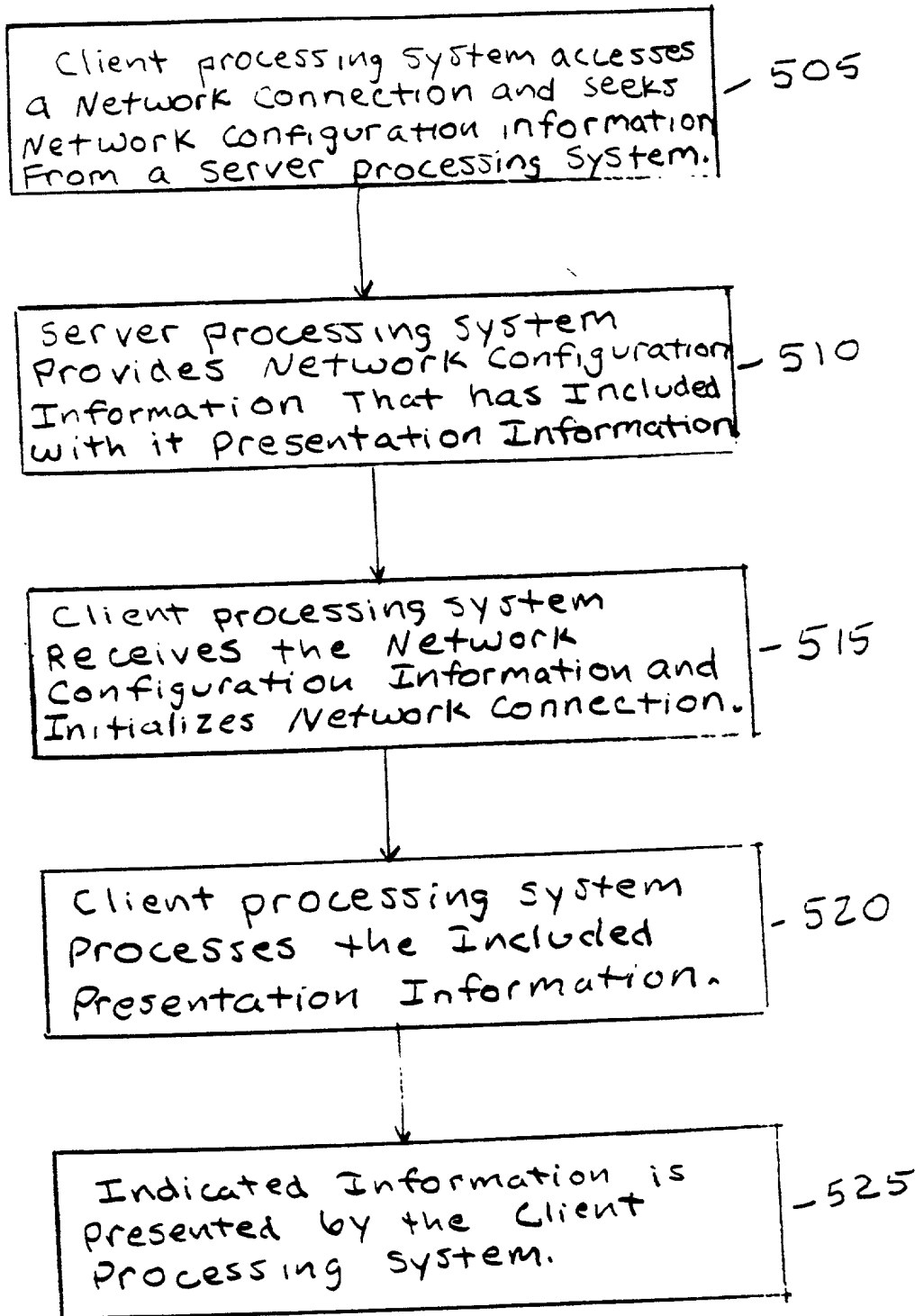


Fig. 5

# Digital Processing System (DPS) Block Diagram

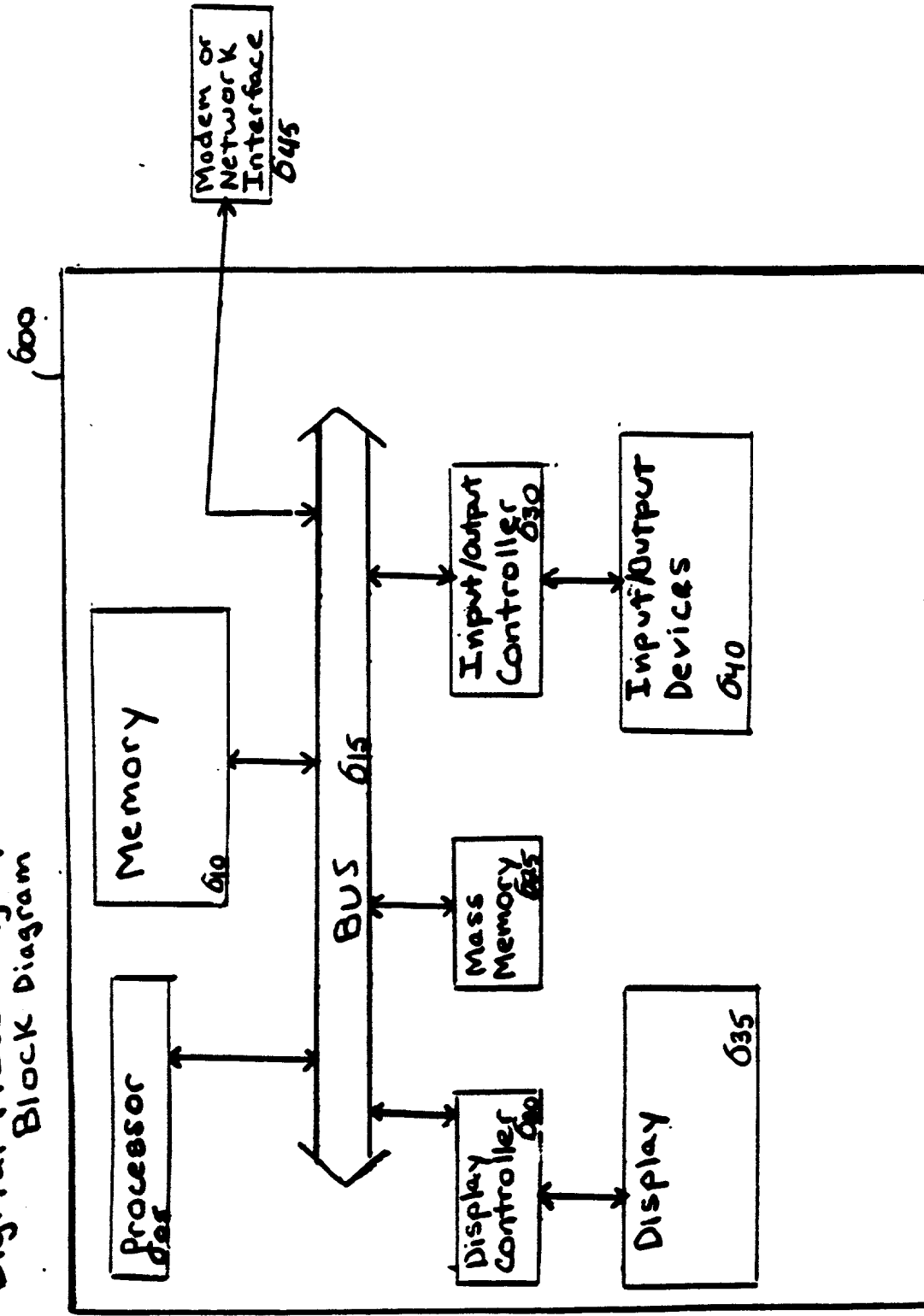


Fig. 6